Features
- Surface Mount Devices
- Lead free device
- Size 5045mm/2018 mils
- Surface Mount packaging for automated assembly

Applications
Almost anywhere there is a low voltage power supply, up to 60V and a load to be protected, including:
- Computer mother board, Modem.
- Telecommunication equipments.

ASMD2018 Series

Pb RoHS

Performance Specification

Model	V_{max} (Vdc)	I_{max} (A)	I_{hold} @ 25℃ (A)	I_{trip} @25℃ (A)	P_d Typ. (W)	Maximum Time To Trip Current (A)	Maximum Time To Trip Time (Sec)	Resistance RI_{min} (Ω)	Resistance $R1_{max}$ (Ω)
ASMD030-2018	60	100	0.30	0.60	0.9	1.5	3.00	0.500	2.300
ASMD050-2018	60	100	0.55	1.20	1.0	2.5	3.00	0.200	1.000
ASMD100-2018	15	100	1.10	2.20	1.1	8.0	0.40	0.060	0.360
ASMD100-33V-2018	33	100	1.10	2.20	1.1	8.0	0.40	0.060	0.360
ASMD150-2018	15	100	1.50	3.00	1.1	8.0	0.80	0.050	0.170
ASMD200-2018	10	100	2.00	4.00	1.1	8.0	2.40	0.030	0.100

Ihold = Hold Current. Maximum current device will not trip in 25℃ still air.
Itrip = Trip Current. Minimum current at which the device will always trip in 25℃ still air.
Vmax = Maximum operating voltage device can withstand without damage at rated current (Imax).
Imax = Maximum fault current device can withstand without damage at rated voltage (Vmax).
Pd = Power dissipation when device is in the tripped state in 25℃ still air environment at rated voltage.
RImin/max = Minimum/Maximum device resistance prior to tripping at 25℃.
$R1_{max}$ = Maximum device resistance is measured one hour post reflow.
CAUTION : Operation beyond the specified ratings may result in damage and possible arcing and flame.

Environmental Specifications

Test	Conditions	Resistance change
Passive aging	+85℃, 1000 hrs.	±5% typical
Humidity aging	+85℃, 85% R.H. , 168 hours	±5% typical
Thermal shock	+85℃ to -40℃, 20 times	±33% typical
Resistance to solvent	MIL-STD-202,Method 215	No change
Vibration	MIL-STD-202,Method 201	No change
Ambient operating conditions :	- 40 ℃ to +85 ℃	
Maximum surface temperature of the device in the tripped state is 125 ℃		

AGENCY APPROVALS : UL pending.

I_{hold} Versus Temperature

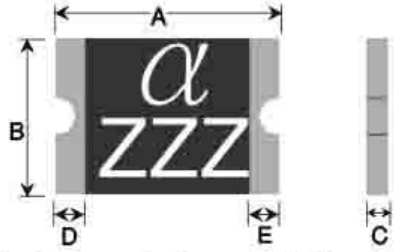
Model	Maximum ambient operating temperature (T_{mao}) vs. hold current (I_{hold}) -40℃	-20℃	0℃	25℃	40℃	50℃	60℃	70℃	85℃
ASMD030-2018	0.48	0.42	0.35	0.30	0.24	0.21	0.17	0.15	0.10
ASMD050-2018	0.87	0.77	0.67	0.55	0.46	0.41	0.36	0.31	0.23
ASMD100-2018	1.71	1.52	1.32	1.10	0.94	0.84	0.74	0.64	0.50
ASMD100-33V-2018	1.71	1.52	1.32	1.10	0.94	0.84	0.74	0.64	0.50
ASMD150-2018	2.38	2.10	1.82	1.50	1.27	1.13	0.99	0.85	0.64
ASMD200-2018	2.95	2.65	2.35	2.00	1.74	1.59	1.44	1.29	1.06

ASMD2018 Series

Construction And Dimension (Unit:mm)

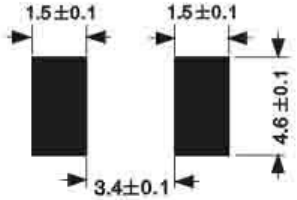
Model	A		B		C		D
	Min.	Max.	Min.	Max.	Min.	Max.	Min.
ASMD030-2018	4.72	5.44	4.22	4.93	0.60	1.10	0.30
ASMD050-2018	4.72	5.44	4.22	4.93	0.60	1.10	0.30
ASMD100-2018	4.72	5.44	4.22	4.93	0.45	0.80	0.30
ASMD100-33V-2018	4.72	5.44	4.22	4.93	0.45	0.80	0.30
ASMD150-2018	4.72	5.44	4.22	4.93	0.45	0.80	0.30
ASMD200-2018	4.72	5.44	4.22	4.93	0.40	0.80	0.30

Dimensions & Marking



α = Trademark
ZZZ = Hold current

Recommended Pad Layout (mm)



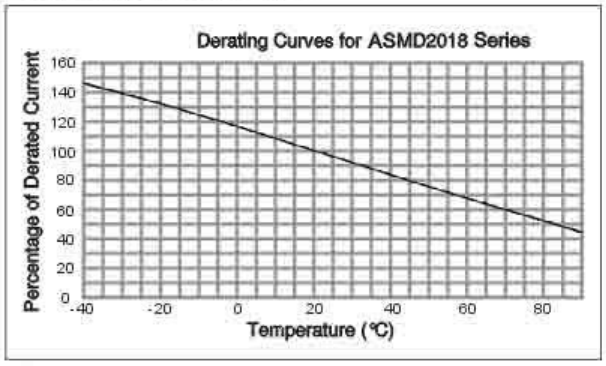
Termination pad characteristics

Terminal pad materials : Gold-Plated Nickel-Copper
Terminal pad solderability : Meets EIA specification RS186-9E and ANSI/J-STD-002 Category 3.

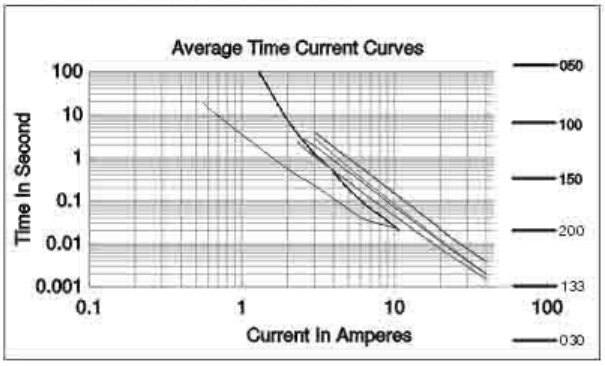
Rework

Use standard industry practices, the removal device must be replaced with a fresh one.

Thermal Derating Curve



Typical Time-To-Trip At 25℃

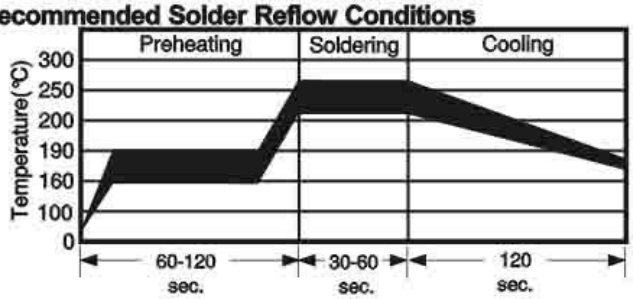


⚠ WARNING:

- Use PPTC beyond the maximum ratings or improper use may result in device damage and possible electrical arcing and flame.
- PPTC are intended for protection against occasional over current or over temperature fault conditions and should not be used when repeated fault conditions or prolonged trip events are anticipated.
- Device performance can be impacted negatively if devices are handled in a manner inconsistent with recommended electronic, thermal, and mechanical procedures for electronic components.
- Use PPTC with a large inductance in circuit will generate a circuit voltage (L dI/dt) above the rated voltage of the PPTC.
- Avoid impact PPTC device its thermal expansion like placed under pressure or installed in limited space.
- Contamination of the PPTC material with certain silicon based oils or some aggressive solvents can adversely impact the performance of the devices. PPTC SMD can be cleaned by standard methods.
- Requests that customers comply with our recommended solder pad layouts and recommended reflow profile. Improper board layouts or reflow profile could negatively impact solderability performance of our devices.

ASMD2018 Series

Recommended Solder Reflow Conditions



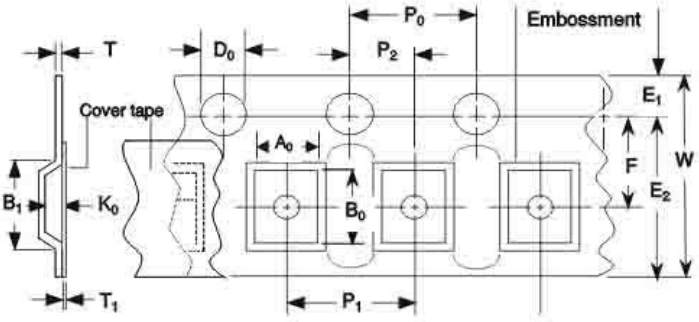
- Recommended reflow methods : IR, vapor phase oven, hot air oven.
- Devices are not designed to be wave soldered to the bottom side of the board.
- Recommended maximum paste thickness is 0.25 mm (0.010 inch).
- Devices can be cleaned using standard method and solvents.

Note : If reflow temperatures exceed the recommended profile, devices may not meet the performance requirements.

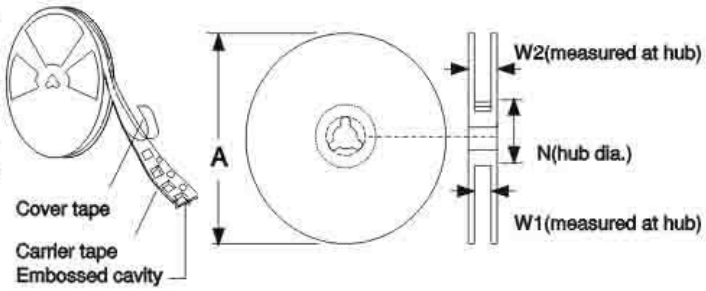
Tape And Reel Specifications (mm)

Governing Specifications	EIA 481-2
W	12.0±0.20
P_0	4.0±0.10
P_1	8.0±0.10
P_2	2.0±0.05
A_0	4.40±0.10
B_0	5.5±0.10
B_1max.	8.2
D_0	1.5+0.1, -0.0
F	5.5±0.05
E_1	1.75±0.10
E_2min.	10.25
Tmax.	0.6
T_1max.	0.1
K_0	1.36±0.1
Leader min.	390
Trailer min.	160
Reel Dimensions	
A max.	178
N min.	50
W_1	12.4+2.0,-0.0
W_2max.	18.4

EIA Tape Component Dimentions



EIA Reel Dimentions



Storage and handling

- Storage conditions : 40℃ max, 70% R.H.
- Devices may not meet specified performance if storage conditions are exceeded.

Order information

ASMD2018	050
Product name	Hold
Size 5045 mm / 2018 mils	Current
SMD : surface mount device	0.50A

Packaging

Tape & Reel Quantity	
030,050	1,500 pcs/reel
100, 100-33V, 150, 200	2,500 pcs/reel

Tape & reel packaging per EIA481-1